

EENG 2310
Electric Circuits Design
Spring 2024
Syllabus

Instructor: Dr. Seyed Mahdi Ghamkhari

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Class Location: ENG 3212

Office Hours: Mondays 3:00 pm to 4:00 pm Faculty Suite – 3100 H

Prerequisites: PHYS 2326

Course Description

Introduction to number system; Boolean algebra; logic operations; combinational logic circuit design; Karnaugh maps; sequential circuit design including registers and counters.

Textbook

M Morris R. Mano, Charles R. Kime and Tom Martin, "Logic & Computer Design Fundamentals, 5th edition", Pearson March 2015, ISBN: 9780133760637

Note: The fifth edition of the textbook will be used in the course.

Link to purchase from Amazon

<https://www.pearson.com/en-us/subject-catalog/p/logic--computer-design-fundamentals/P200000003256/9780133760637>

Course Components

Assignments (Homework/Quizzes/Projects): 40%

Midterm Exam: 30%

Final Exam: 30%

Conversion of the numerical grade to letter grade

A: 90 – 100%

B: 80 – 89%

C: 70 – 79%

D: 60 – 69%

F: 0 – 59%

Note: Final grades will be rounded to the nearest whole number. For example, 81.5 or 81.6 would be rounded to 82, but 81.4 would be rounded to 81.

Dates

Last Day to Drop without Creating a Record: 1/31/24

Last Day to Drop or Withdraw: 3/29/24

Guideline

- Students must have the required prerequisites of the course.
- Late submissions to the assignments are not accepted and a grade of zero will be allocated.
- Makeup exams will not be given to students who are absent from the exams.
- Canvas is the platform for course announcements and assignments.
- Announcements are made on Canvas.
- Assignments are posted on Canvas and students' responses to the assignments should be uploaded on Canvas.
- Students are highly recommended to configure their Canvas account so that they could be notified immediately once an announcement or assignment is posted.
- Students should communicate with the instructor using their UTPB Email address.
- Requests for re-grading must be received no later than one week following the return of the assignment.
- Students are encouraged to take notes from the materials presented in the class.

UTPB Student Conduct and Discipline

<https://www.utpb.edu/life-at-utpb/student-services/dean-of-students/student-conduct-and-discipline-utpb-revised-2020-final.pdf>

Attendance

Attendance in class is required.

Students with Disabilities

The University of Texas Permian Basin in compliance with the Americans with Disabilities Act and Section 504 of the Rehabilitation Act provides "reasonable accommodations" to students with disabilities. Only those students, who an instructor has received an official Letter of Accommodation (LOA) sent by the office of ADA for students, will be provided ADA academic accommodations.

ADA Office for Students

MR. Paul Leverington

Address: Mesa Building 4242, 4901 E. University, Odessa, Texas 79762

Phone: 4325524696

Email: ada@utpb.edu

Topics

1. Digital Systems and Information
2. Combinational Logic Circuits
3. Combinational Logic Design
4. Sequential Circuits
5. Digital Hardware Implementation
6. Registers and Register Transfers

- 7. Memory Basics
- 8. Computer Design Basics

Outline

- Week 1-2: Digital Systems and Information
- Week 3-4: Combinational Logic Circuits
- Week 5-6: Combinational Logic Design
- Week 7-8: Sequential Circuits
- Week 9: Spring break
- Week 10: Digital Hardware Implementation
- Week 11-12: Registers and Register Transfers
- Week 13-14: Memory Basics
- Week 15-16: Computer Design Basics

Disclaimer: The subject matter and dates for the course may evolve slightly and should be considered tentative. Updates will be announced.

Version

01/16/2024